

ABSTRACT OF THE DISCLOSURE

A UTOPIA (Universal test and operations PHY interface for ATM) interface control device for connecting an Asynchronous Transfer Mode (ATM) layer function to a physical (PHY) layer function at UTOPIA level 2 is provided. The device comprises N groups of 32 PHY layer functions disposed according to a number of UTOPIA addresses, a unit connected between the ATM layer function and the N groups of 32 PHY layer functions, for allowing the ATM layer function to simultaneously deliver a UTOPIA address to the N groups of 32 PHY layer functions, a unit connected between the ATM layer function and each of the N groups of PHY layer functions, for allowing each of the N groups of 32 PHY layer functions to deliver a cell available signal to the ATM layer function, and a unit connected between the ATM layer function and each of the N groups of 32 PHY layer functions, for allowing the ATM layer function to deliver an enable signal to each of the N groups of 32 PHY layer functions.

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